

connection locations in each row to be offset or staggered from each adjacent row such that an array of spaces is formed with each space in an interior row forming a center of a diamond shaped four connection point connector that has a connection location to a conductive strip at each of it's four points or corners that originates from a different conductive strip.

This is the essence of Applicant's invention and distinguishes Applicant's circuit board from all the prior art cited.

Applicant submits that the subject application is in condition for allowance and an early and favorable action to that end is requested.

Respectfully submitted,

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INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				Applicant Paul A. Swetland		Filing Date September 16, 2003		Group No. 2841	

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U.S. PATENT DOCUMENTS							
Examiner Initial*	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	1	6,650,548 B1	11-18-03	Swetland	361	780	

FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		

Examiner	Date Considered
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*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.